

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:	Airey et al.	Examiner:	Wang, Jin-Cheng
Serial No.:	09/614,363	Art Unit:	2628
Date Filed:	July 12, 2000	Conf. No.:	2211
Title:	DISPLAY SYSTEM HAVING FLOATING POINT RASTERIZATION AND FLOATING POINT FRAMEBUFFERING		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**DECLARATION OF STEPHEN MOFFITT UNDER 37 CFR §1.132**

Dear Sir:

In support of the accompanying response to the February 20, 2008 office action in the above-referenced application, I hereby declare as follows:

1. I have been employed at Silicon Graphics, Inc. ("SGI") since 1986.
2. Prior to the filing date (September 25, 1997) of US Patent No. 6,567,083 (the "Patent"), my title was Director of Graphics Software for SGI. In that capacity, I was familiar with all the major graphics projects within SGI, and the engineering development processes within SGI.
3. Prior to the filing date of the Patent, John Airey worked on a machine that used floating point rasterization, including floating point scan conversion and a floating point framebuffer. The internal code name for the machine at SGI was "*Bali*." The subject patent application (the "Application") describes and claims various aspects of Bali.

4. Prior to the filing date of the Patent, there were no other independent projects in SGI that involved floating point rasterization, including floating point scan conversion and a floating point framebuffer. If any other such projects existed at that time within SGI, they were derived from Mr. Airey's work in this area.

5. Daniel Baum, one of the inventors of the Patent, was John Airey's supervisor for the Bali project. In this capacity, Mr. Baum was the Engineering Director of the Bali project.

6. I have reviewed the documents discussed in paragraphs 6-11 of the Declaration of John M. Airey Under 37 CFR 1.132 submitted herewith. Prior to the filing date of the Application, it was normal procedure in SGI for Mr. Baum, the Engineering Director, to have access to such documents.

7. Mr. Airey and other inventors of the Application wrote a software simulator to review features for possible implementation in the Bali product.

8. Matthew Pharr, a co-inventor of the Patent and working under the supervision of Mr. Baum, modified this software simulator to test shading technologies (among other things), which are the subject of the Patent. Below in paragraphs 9-16 is a list of documents showing Mr. Pharr's use of Mr. Airey's simulation software.

9. ri\_shader.c – a document showing shader simulation code and attached as Exhibit A.

10. RCS log for ri\_shader.c – attached as Exhibit B, a log document showing the dates and times that the ri\_shader.c document was "checked-in" (i.e., accessed). The initials "mmp," which are Mr. Pharr's initials as well as his unique login identification for access to

SGI's RCS source code trees, show that he checked in the `ri_shader.c` document before the filing date of the Patent, such as on September 4, 1997.

11. `ri_block.c` – a document showing block simulation code and attached as Exhibit C.

12. RCS log for `ri_block.c` – attached as Exhibit D, a log document showing the dates and times that the `ri_block.c` document was “checked-in.” The initials “mmp” show that Mr. Pharr checked in the `ri_block.c` document on various dates before the filing date of the Patent, such as on September 17, 1997, September 18, 1997, and September 23, 1997.

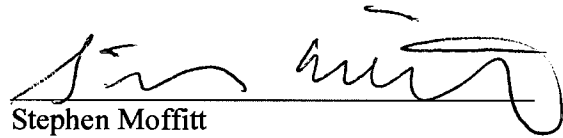
13. `ri_xform.c` – a document showing transform simulation code and attached as Exhibit E.

14. RCS log for `ri_xform.c` – attached as Exhibit F, a log document showing the dates and times that the `ri_xform.c` document was “checked-in.” The initials “mmp” show that Mr. Pharr checked in the `ri_xform.c` document before the filing date of the Patent, such as on September 17, 1997.

15. `ri_light.c` – a document showing lighting simulation code and attached as Exhibit G.

16. RCS log for `ri_light.c` – attached as Exhibit H, a log document showing the dates and times that the `ri_light.c` document was “checked-in.” The initials “mmp” show that Mr. Pharr checked in the `ri_light.c` document on various dates before the filing date of the Patent, such as on September 3, 1997, September 4, 1997, September 5, 1997, September 8, 1997, September 9, 1997, September 11, 1997, September 17, 1997, September 18, 1997, September 23, 1997, and September 24, 1997.

17. I hereby declare that all statements made herein of my own knowledge are true, that all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

  
Stephen Moffitt

Dated: 4/28/2008

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